

SP6T 18GHz

On load

Normally open / Latching

◆ RF Features

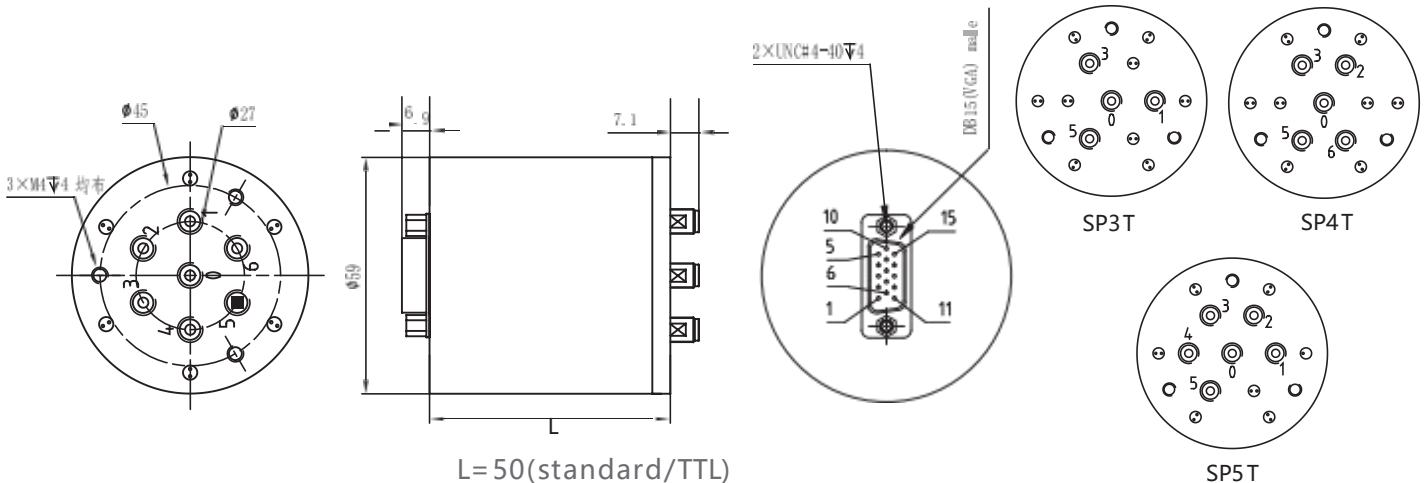
RF Range (GHz)	Insertion loss (dB)	Isolation (dB)	Standing wave
DC -6	0.3	70	1.3
6 - 12	0.4	60	1.4
12 - 18	0.5	50	1.5

◆ Operating voltage/current

Operating voltage (V)		12	24	28
Current(mA)	Normally Open	300	200	180
	Latching	320	200	180

* The voltage can be selected according to user's requirements.

◆ Product dimensions



◆ Product features

- DC to 18GHz
- Low SWR, low loss, high isolation
- Connector form SMA
- TTL level control is selectable

◆ Technical specifications

Switching sequence: first break and then close

Switching rate: <15ms

Operating temperature:

-25°C~65°C (standard)

-55°C~85°C(Temperature expansion)

Switching life: 2 million times

RF connector: SMA Female

Control interface: DB15 Male

Impact (non-working state): 30G, 1/2 Sine, 11ms

Vibration (operating state): 20-2000 Hz, 10G

RMS

SP6T 26.5GHz

On load

Normally open / Latching

◆ RF Features

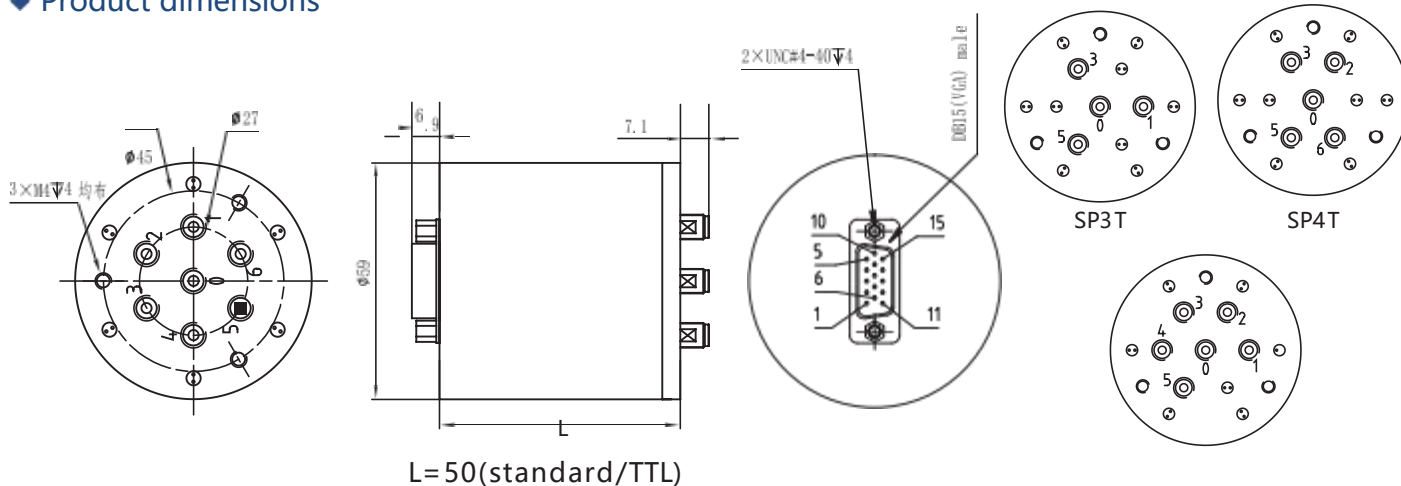
RF Range (GHz)	Insertion loss (dB)	Isolation (dB)	Standing wave
DC -6	0.3	70	1.3
6 -12	0.4	60	1.4
12 - 18	0.5	50	1.5
18 -26.5	0.6	50	1.6

◆ Operating voltage/current

Operating voltage (V)		12	24	28
Current(mA)	Normally Open	300	200	180
	Latching	320	200	180

* The voltage can be selected according to user's requirements.

◆ Product dimensions



◆ Product features

- DC to 26.5GHz
- Low SWR, low loss, high isolation
- Connector form SMA
- TTL level control is selectable

Technical specifications

Switching sequence: first break and then close Switching life: 2 million times

Switching rate: <15ms

RF connector: SMA Female

Operating temperature:

Control interface: DB15 Male

-25°C~65°C (standard)

-55°C~85°C(Temperature expansion)

Impact (non-working state): 30G, 1/2 Sine,
11ms Vibration (operating state): 20-
2000 Hz、10G RMS

SP6T 40GHz

On load

Normally open / Latching

◆ RF Features

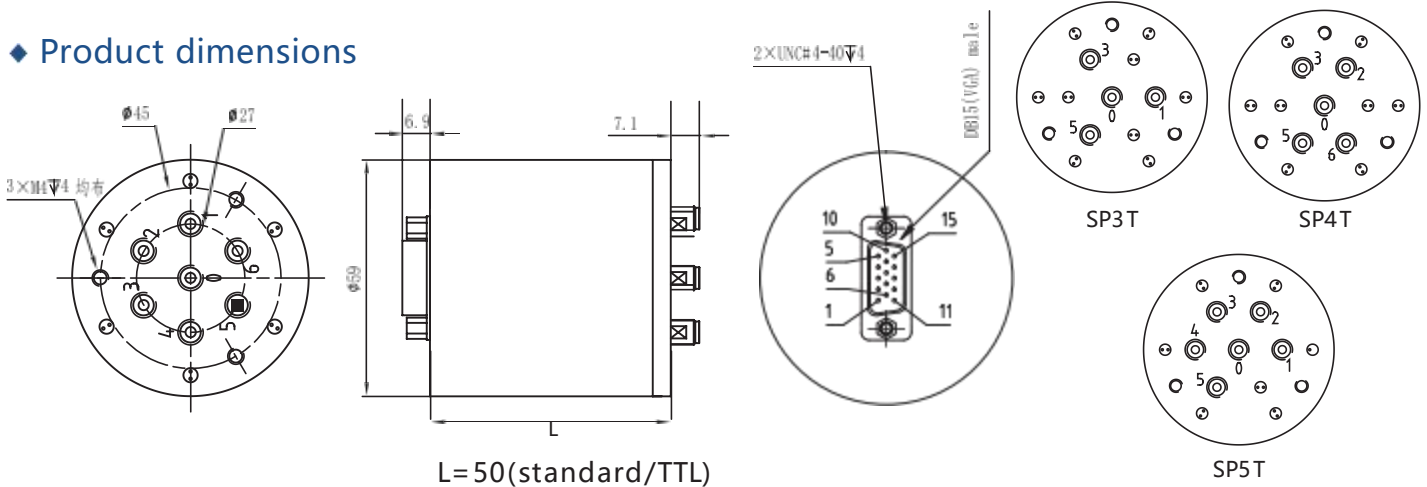
RF Range (GHz)	Insertion loss (dB)	Isolation (dB)	Standing wave
DC -6	0.3	70	1.3
6 -12	0.4	60	1.4
12 - 18	0.5	55	1.5
18 -26.5	0.7	55	1.7
26.5 - 32	0.8	50	1.8
32 - 40	0.9	50	1.9

◆ Operating voltage/current

Operating voltage (V)		12	24	28
Current (mA)	Normally Open	300	200	180
	Latching	320	200	180

* The voltage can be selected according to user's requirements.

◆ Product dimensions



◆ Product features

- DC to 40GHz
- Low SWR, low loss, high isolation
- Connector form 2.92
- TTL level control is selectable

◆ Technical specifications

Switching sequence: first break and then close

Switching rate: <15ms

Operating temperature:

-25°C~65°C (standard)

-55°C~85°C (Temperature expansion)

Switching life: 2 million times

RF connector: 2.92 Female

Control interface: DB15 Male

Impact (non-working state): 30G, 1/2 Sine, 11ms

Vibration (operating state): 20-2000Hz, 10GRMS

SP6T 50GHz

On load

Normally open / Latching

◆ RF Features

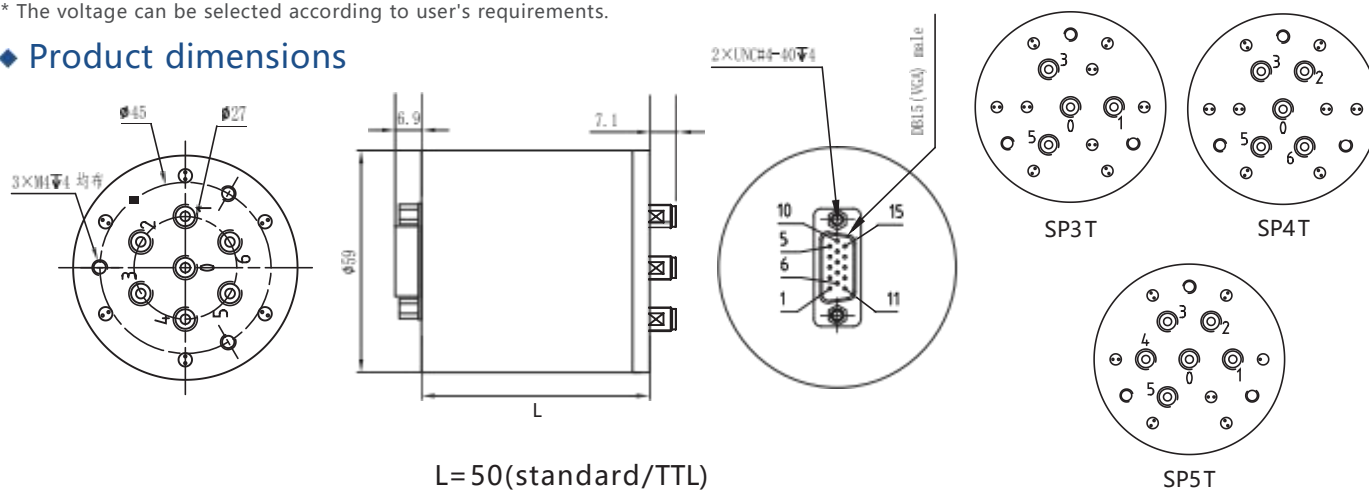
RF Range (GHz)	Insertion loss (dB)	Isolation (dB)	Standing wave
DC -6	0.3	70	1.3
6 - 12	0.4	60	1.4
12 - 18	0.5	55	1.5
18 -26.5	0.7	55	1.7
26.5 - 32	0.9	50	1.9
32 - 40	1.0	50	2.0
40 - 50	1.2	45	2.2

◆ Operating voltage/current

Operating voltage (V)		12	24	28
Current(mA)	Normally Open	300	200	180
	Latching	320	200	180

* The voltage can be selected according to user's requirements.

◆ Product dimensions



◆ Product features

- DC to 50GHz
- Low SWR, low loss, high isolation
- Connector form 2.4
- TTL level control is selectable

◆ Technical specifications

Switching sequence: first break and then close

Switching rate: <15ms

Operating temperature:

-25°C~65°C (standard)

-55°C~85°C(Temperature expansion)

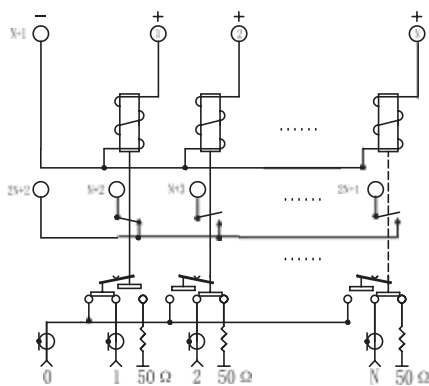
Switching life: 2 million times

RF connector: 2.4 Female

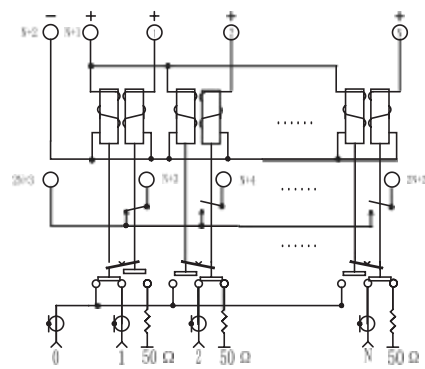
Control interface: DB15 Male

Impact (non-working state): 30G, 1/2 Sine, 11ms

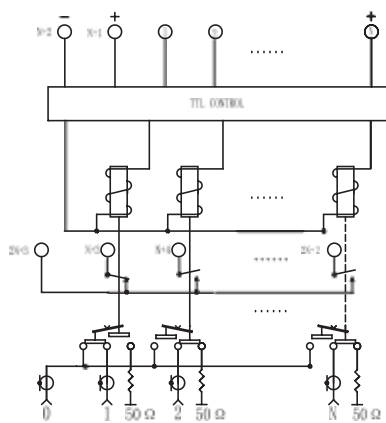
Vibration (operating state): 20-2000Hz, 10GRMS



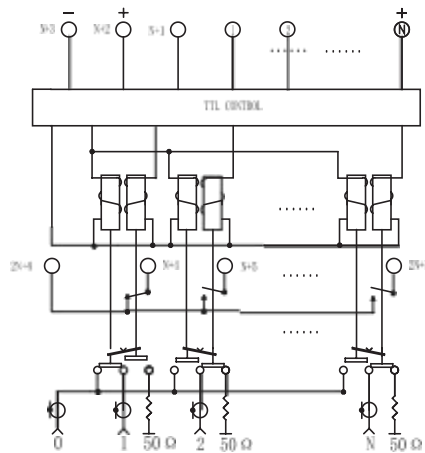
Normally Open



Latching



Normally Open+TTL



Latching+TTL

Pin definition					
Switching method SPnT, n(3~6)		RF Channel	DB15/DB25 MALE		
			Motivation	Feedback	
Normally open		NO TTL	0→1	1:VDC, n+1:GND	2n+2→ n+2
			0→2	2:VDC, n+1:GND	2n+2→ n+3
			0→n	n:VDC, n+1:GND	2n+2→2n+1
		TTL	0→1	1:TTL, n+1:VDC, n+2:GND	2n+3→ n+3
			0→2	2:TTL, n+1:VDC, n+2:GND	2n+3→ n+4
			0→n	n:TTL, n+1:VDC, n+2:GND	2n+3→2n+2
Latching	NO TTL	0→1	1:VDC, n+1:VDC, n+2:GND	2n+3→ n+3	
		0→2	2:VDC, n+1:VDC, n+2:GND	2n+3→ n+4	
		0→n	n:VDC, n+1:VDC, n+2:GND	2n+3→2n+2	
	TTL	0→1	1:TTL, n+1:TTL, n+2:VDC, n+3:GND	2n+4→ n+4	
		0→2	2:TTL, n+1:TTL, n+2:VDC, n+3:GND	2n+4→ n+5	
		0→n	n:TTL, n+1:TTL, n+2:VDC, n+3:GND	2n+4→2n+3	

Note: Latching switch should RESET pin n+1 before excitation.